## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	)
Apel et al.	) ) Examiner: Matthew E. Warrer ) Art Unit: 2815
Application No: 10/769,571	
Filed: January 30, 2004	
For: BIPOLAR JUNCTION TRANSISTOR GEOMETRY	) ) )

## **RESPONSE**

## Dear Examiner:

In response to the Office Action mailed October 19, 2009, please consider the following remarks.